

ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1

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Title of Invention	FAST-DISABLED VOLTAGE REGULATOR CIRCUIT WITH LOW-NOISE FEEDBACK LOOP AND OPERATING METHOD THEREOF																																																																																																										
<p>Submission Type : Utility Patent Filing</p> <p>Application Number: 10/711618 </p> <p>EFS ID: 69453</p> <table border="1" style="width: 100%; border-collapse: collapse; text-align: center;"> <thead> <tr> <th style="width: 25%;">Confirmation Code</th> <th style="width: 75%;">Message</th> </tr> </thead> <tbody> <tr> <td>ISVR1</td> <td>Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application</td> </tr> <tr> <td>ICON1</td> <td>5617</td> </tr> <tr> <td>USPTOEFSNote</td> <td>For assistance with e-filing a patent application, contact the Patent Electronic Business Center: Toll-Free Number: 1(866) 217-9197 Website: http://www.uspto.gov/ebc/</td> </tr> </tbody> </table> <p>First Named Applicant: Ling-Wei Ke</p> <p>Attorney Docket Number: MTKP0080USA</p> <p>Timestamp: 2004-09-29 04:00:59 EDT</p> <p>From: US</p> <p>File Listing:</p> <table border="1" style="width: 100%; border-collapse: collapse; text-align: center;"> <thead> <tr> <th style="width: 25%;">Doc. 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